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DRAFT

AMENDMENTS TO THE CLAIMS

Kindly amend the claims as follows:

1 -41 (Cancelled)

42. (Cancelled)

43. (Previously Presented) An integrated circuit design library which provides design elements for placing into a design of an integrated circuit, the library comprising:

a set of selectable transmission line topologies selectable as design elements for critical interconnect lines of an integrated circuit being designed, said lines capable of carrying analog and mixed signals, which topologies comprise return paths therein; and

a set of parametrized, equivalent RLC ladder networks, one per topology.

44. (Previously Presented) The library according to claim 43 and also comprising means for performing both frequency and time domain analysis for each transmission line placed into an integrated circuit design.

45. (Previously Presented) The library according to claim 43 and wherein at least an inductance parameter of each said transmission line topology is a function of frequency

46. (Cancelled)

47. (Previously Presented) The library according to claim 45 and wherein said frequency ranges from DC to a transistor cut-off frequency.

48. (Cancelled).